Model-Free Wide-Area Monitoring of Power Grids via Cutset Voltages

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John W. Simpson-Porco & Nima Monshizadeh





Wide-Area Monitoring and the Power Grid



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- Graph $\mathcal{G} = (\mathcal{N}, \mathcal{E})$ with edge weights g_{ij} , $g = \operatorname{diag}(g_{ij})$
- Incidence matrix: $A \in \{-1, 0, 1\}^{(nodes) \times (edges)}$
- Nodal current injections *l_i* and edge currents *i_{ij}*
- Nodal voltages V_i and branch voltages v_{ij}

Circuit Equations I = Ai (KCL) $v = A^{T}V$ (KVL) i = gv (Ohm)

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Basic Circuit Theory for Two-Area Circuit



• Nodal partitioning: $\mathcal{N} = \mathcal{N}_a \cup \mathcal{N}_b$

$$I = \begin{bmatrix} I_a \\ I_b \end{bmatrix}, \qquad V = \begin{bmatrix} V_a \\ V_b \end{bmatrix} \text{ and } \begin{bmatrix} I_a \\ I_b \end{bmatrix} = \begin{bmatrix} G_{aa} & G_{ab} \\ G_{ba} & G_{bb} \end{bmatrix} \begin{bmatrix} V_a \\ V_b \end{bmatrix}$$

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Dobson et. al.

• A scalar, aggregated metric for stress across a cutset

• Define the cutset current

$$i_{\mathrm{cut}} = \mathbb{1}^{\mathsf{T}} i_{ab} = \sum_{e \sim (i,j) \in \mathcal{E}^{ab}} i_e$$
.

• and the cutset conductance

$$g_{\mathrm{cut}} = \mathbb{1}^{\mathsf{T}} g_{ab} \mathbb{1} = \sum_{(i,j) \in \mathcal{E}^{ab}} g_{ij},$$

$$v_{
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• Voltage across "average" edge in cutset

• $g_{\rm cut}$ is **parallel** combination of conductances in cut

Exact reduction if voltages uniform on either side of cut!

Decompose voltage vectors as

$$\begin{bmatrix} V_a \\ V_b \end{bmatrix} = \begin{bmatrix} \mu_a \mathbb{1}_a \\ \mu_b \mathbb{1}_b \end{bmatrix} + \begin{bmatrix} \widetilde{V}_a \\ \widetilde{V}_b \end{bmatrix}, \qquad (1)$$



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Result: Cutset Voltage and Average Voltage Levels Define weighted inter-area degree vectors

$$g_a = -G_{ab}\mathbb{1}_b$$
 $g_b = -G_{ba}\mathbb{1}_a$

and the projection matrices

$$\Pi_a = \mathbb{I}_a - \frac{1}{|\mathcal{N}_a|} \mathbb{1}_a \mathbb{1}_a^{\mathsf{T}}, \quad \Pi_b = \mathbb{I}_b - \frac{1}{|\mathcal{N}_b|} \mathbb{1}_b \mathbb{1}_b^{\mathsf{T}},$$

Theorem 1

The cutset voltage $v_{\rm cut}$ is given by

$$v_{\rm cut} = \mu_a - \mu_b + \varepsilon_{\rm cut}$$

where the *cutset error* ε_{cut} is

$$\varepsilon_{\rm cut} = \frac{1}{g_{\rm cut}} \begin{bmatrix} g_a^{\mathsf{T}} \Pi_a & -g_b^{\mathsf{T}} \Pi_b \end{bmatrix} \begin{bmatrix} V_a \\ V_b \end{bmatrix},$$

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The error term will vanish if:

• Uniform Area Voltages: $V_a = \mu_a \mathbb{1}_a$ and $V_b = \mu_b \mathbb{1}_b$ $\implies \epsilon_{cut} = 0$

•
$$\Pi_a g_a = 0$$
 and $\Pi_b g_b = 0$ (?)

Almost Equitable Partitions I

Definition

Let $\pi \triangleq \{\mathcal{N}_1, \mathcal{N}_2, \dots, \mathcal{N}_K\}$ be a partition of $\mathcal{G} = (\mathcal{N}, \mathcal{E})$. For any node $i \in \mathcal{N}$ and any area $q \in \{1, \dots, K\}$, let

$$g_{\text{parallel}}(i, \mathcal{N}_q) \triangleq \sum_{j \in \mathcal{N}_q, \{i, j\} \in \mathcal{E}} g_{ij},$$

be the total parallel conductance between node $i \in \mathcal{N}$ and area \mathcal{N}_q . We call π an *almost equitable partition* (AEP) of \mathcal{G} if for each pair of distinct areas $p, q \in \{1, \ldots, K\}$, there exists a value $g_{pq} \in \mathbb{R}$ such that

$$g_{\text{parallel}}(i, \mathcal{N}_q) = g_{pq},$$

for all nodes $i \in \mathcal{N}_p$.

• Every node in each area "sees" the same parallel conductance to the other areas.

Almost Equitable Partitions II



• Conditions for AEP?



In this example:

$$g_a = \begin{bmatrix} 5\\5\\5\\5\\5\end{bmatrix}$$

Almost Equitable Partitions II



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Lemma

For two-area network $\pi = \{\mathcal{N}_{a}, \mathcal{N}_{b}\}$:

 π is an AEP

 $g_a \in \operatorname{im}(\mathbb{1}_a)$ and $g_b \in \operatorname{im}(\mathbb{1}_b)$

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In this example:

$$g_a = \begin{bmatrix} 5\\5\\5\\5\end{bmatrix}$$
 $g_b = \begin{bmatrix} 10\\10\end{bmatrix}$

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- Uniform Area Voltages: $V_a = \mu_a \mathbb{1}_a$ and $V_b = \mu_b \mathbb{1}_b$ $\implies \varepsilon_{\text{cut}} = 0$
- Almost Equitable Partition: $\pi = \{N_a, N_b\}$

$$\implies g_a, g_b \in \operatorname{im}(\mathbb{1})$$
$$\implies \varepsilon_{\operatorname{cut}} = 0$$

Example



$$\mu_a = \frac{V_1 + V_2 + V_3 + V_4}{4}$$
$$\mu_b = \frac{V_5 + V_6}{2}$$

$$\searrow$$





Extension to Voltage Across An Area



Key Idea: Kron reduction

$$\begin{bmatrix} I_{a} \\ I_{m} \\ I_{b} \end{bmatrix} = \begin{bmatrix} G_{aa} & G_{am} & \mathbb{O} \\ G_{ma} & G_{mm} & G_{mb} \\ \mathbb{O} & G_{bm} & G_{bb} \end{bmatrix} \begin{bmatrix} V_{a} \\ V_{m} \\ V_{b} \end{bmatrix} \implies \begin{bmatrix} I_{red} \\ I_{red} \\ I_{b}^{red} \end{bmatrix} = \begin{bmatrix} G_{aa}^{red} & G_{ab}^{red} \\ G_{ba}^{red} & G_{bb}^{red} \end{bmatrix} \begin{bmatrix} V_{a} \\ V_{b} \end{bmatrix}$$

Voltage Across an Area Dobson *et. al.*

We now have a reduced graph $\mathcal{G}_{red} = (\mathcal{N}_a \cup \mathcal{N}_b, \mathcal{E}_{red})$

• Define the current across the area

$$i_{ ext{cut}}^{ ext{red}} = \sum\nolimits_{e \sim (i,j) \in \mathcal{E}_{ ext{red}}^{ab}} i_e \, .$$

• and the area conductance

$$g_{ ext{area}} = \sum\nolimits_{(i,j) \in \mathcal{E}^{ab}_{ ext{red}}} g^{ ext{red}}_{ij} \, ,$$

• Leading to the area voltage

$$v_{
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$$v_{\rm area} = i_{\rm cut}^{\rm red}/g_{\rm area}$$



Decomposing the Area Voltage

• Applying our cutset results, we have

Theorem 2

The area voltage $v_{\rm cut}$ is given by

$$\nu_{\rm area} = \mu_{a} - \mu_{b} + \varepsilon_{\rm area}$$

where the area error $\varepsilon_{\rm area}$ is

$$\varepsilon_{\text{area}} = \frac{1}{g_{\text{area}}} \begin{bmatrix} (g_a^{\text{red}})^{\mathsf{T}} \Pi_a & -(g_b^{\text{red}})^{\mathsf{T}} \Pi_b \end{bmatrix} \begin{bmatrix} V_a \\ V_b \end{bmatrix},$$

• We know $\varepsilon_{area} = 0$ if $\pi_{red} = \{N_a, N_b\}$ is an A.E.P. of the reduced graph \mathcal{G}_{red} .

$$\Longrightarrow$$
 Relationship to original graph \mathcal{G} ?





 \implies A.E.P. preserved under Kron Reduction

 $v_{\text{area}} = \mu_a - \mu_b$ if $\{\mathcal{N}_a, \mathcal{N}_m, \mathcal{N}_b\}$ is an A.E.P.

 \implies Calculate $v_{\rm area}$ from pure voltage measurements



 \mathcal{N}_{a}

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 \mathcal{N}_m

Conclusions

Theoretical development for cutset and area voltage:

- **1** $v_{\rm cut} = ({\rm diff.} \text{ in mean voltage levels}) + {\rm remainder } \varepsilon_{\rm cut}$
- Remainder vanishes for almost equitable partitions
- Iso holds for voltage across area
- In Kron Reduction preserves A.E.P.

Open Problems:

- Optimal cutset selection, "spanning cutsets"
- ② Refine treatment of border buses
- 3 Refine analysis for multi-area networks

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https://ece.uwaterloo.ca/~jwsimpso/ jwsimpson@uwaterloo.ca appendix

(Simplified, Linearized, and Network-Reduced)

Grid: $\mathcal{G} = (\mathcal{V}, \mathcal{E}, B)$

Swing Dynamics $\dot{\theta}_i = \omega_i$ $M_i \dot{\omega}_i = -D_i \omega_i + P_i^* - P_{e,i}(\theta) + p_i$

(Linearized) Power Flow

$$P_{e,i}(\theta) = \sum_{j=1}^{n} B_{ij}(\theta_i - \theta_j),$$

Problem: How to pick controls p_i for (i) $\omega_{ss} = 0$ (ii) optimality?



OFR Problem
minimize
$$\sum_{i=1}^{n} \frac{1}{2} k_i p_i^2$$

subject to $\sum_{i=1}^{n} (P_i^* + p_i) = 0$
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Smart Grid Project Samples Distributed Inverter Control



Voltage Collapse (Nat. Comms.)



Optimal Distrib. Volt/Var (CDC)



Wide-Area Monitoring (TSG)

